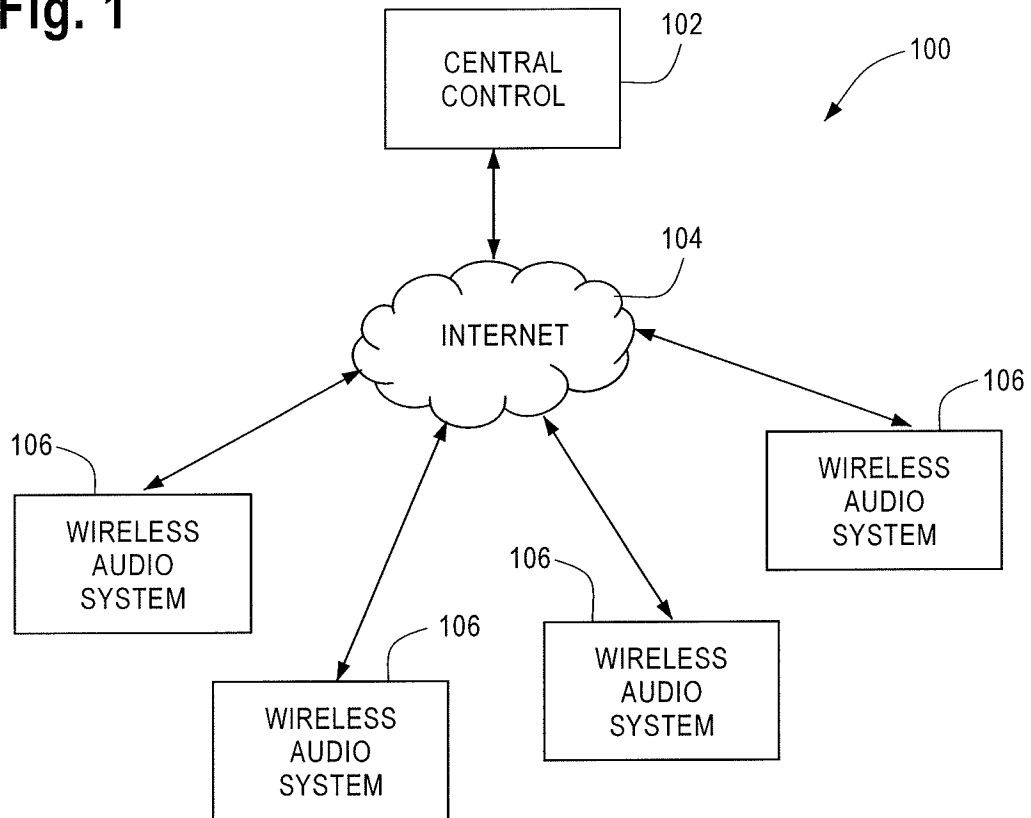
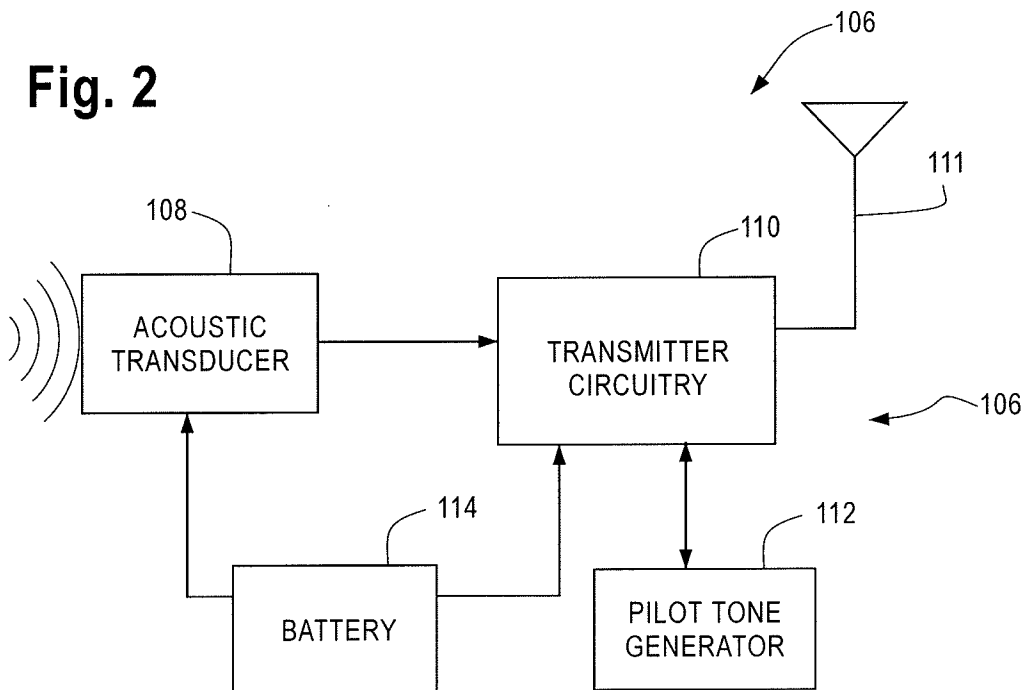


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**Fig. 1**

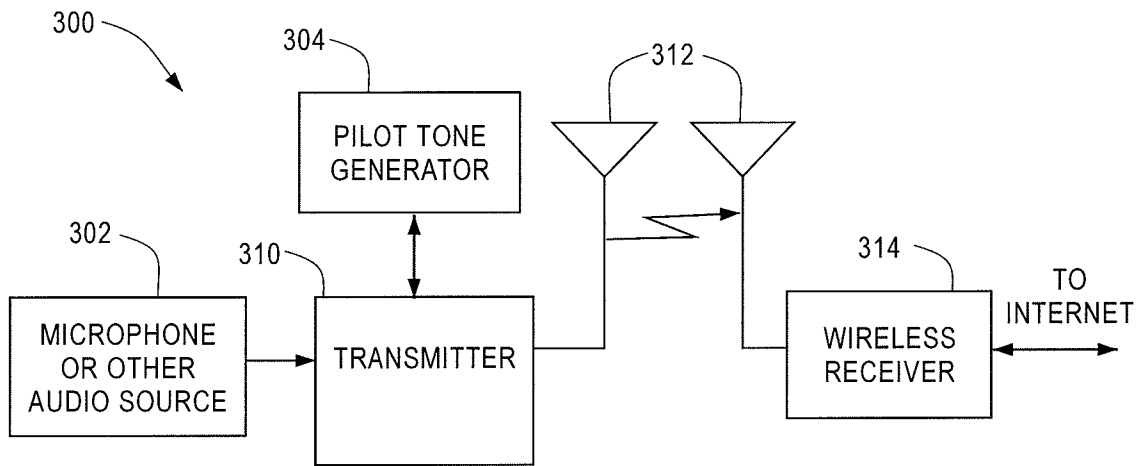


**Fig. 2**

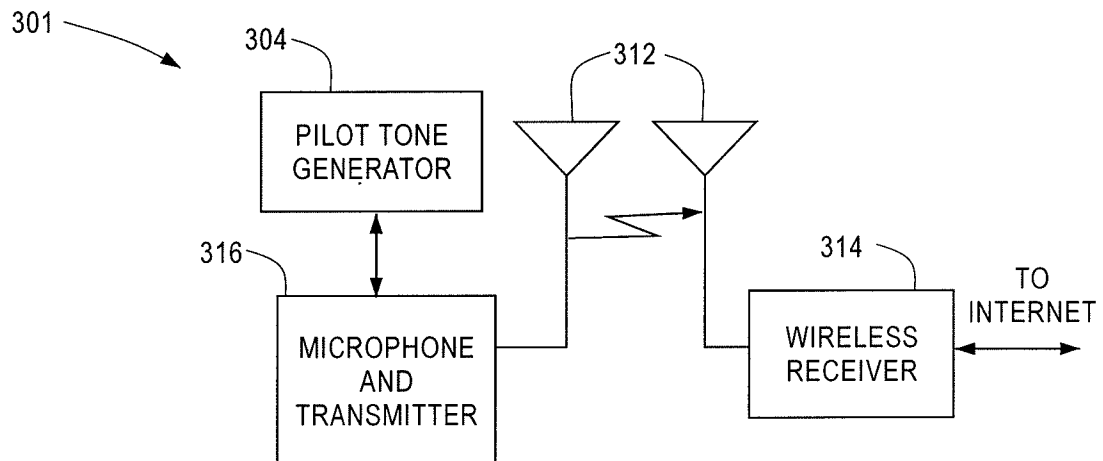


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**Fig. 3A**

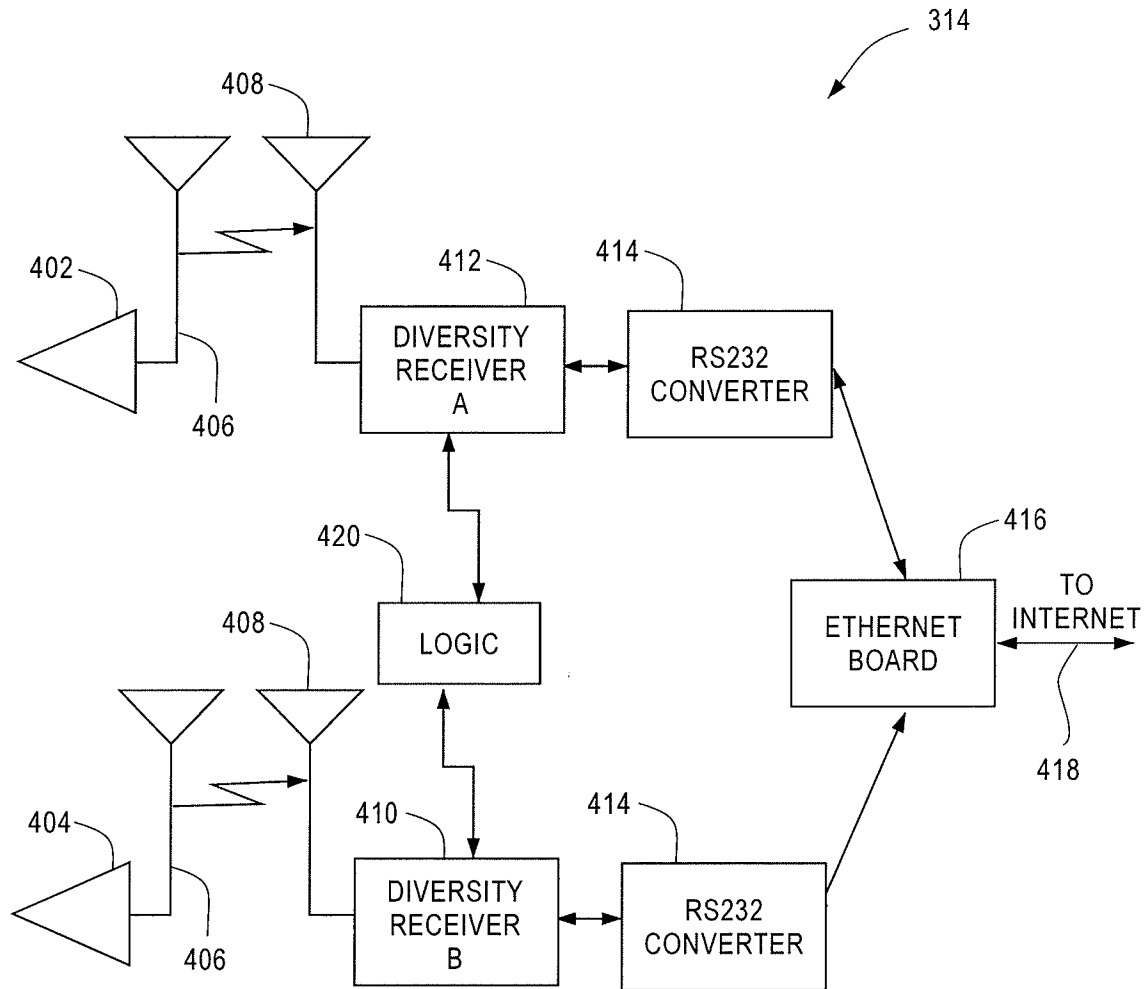


**Fig. 3B**



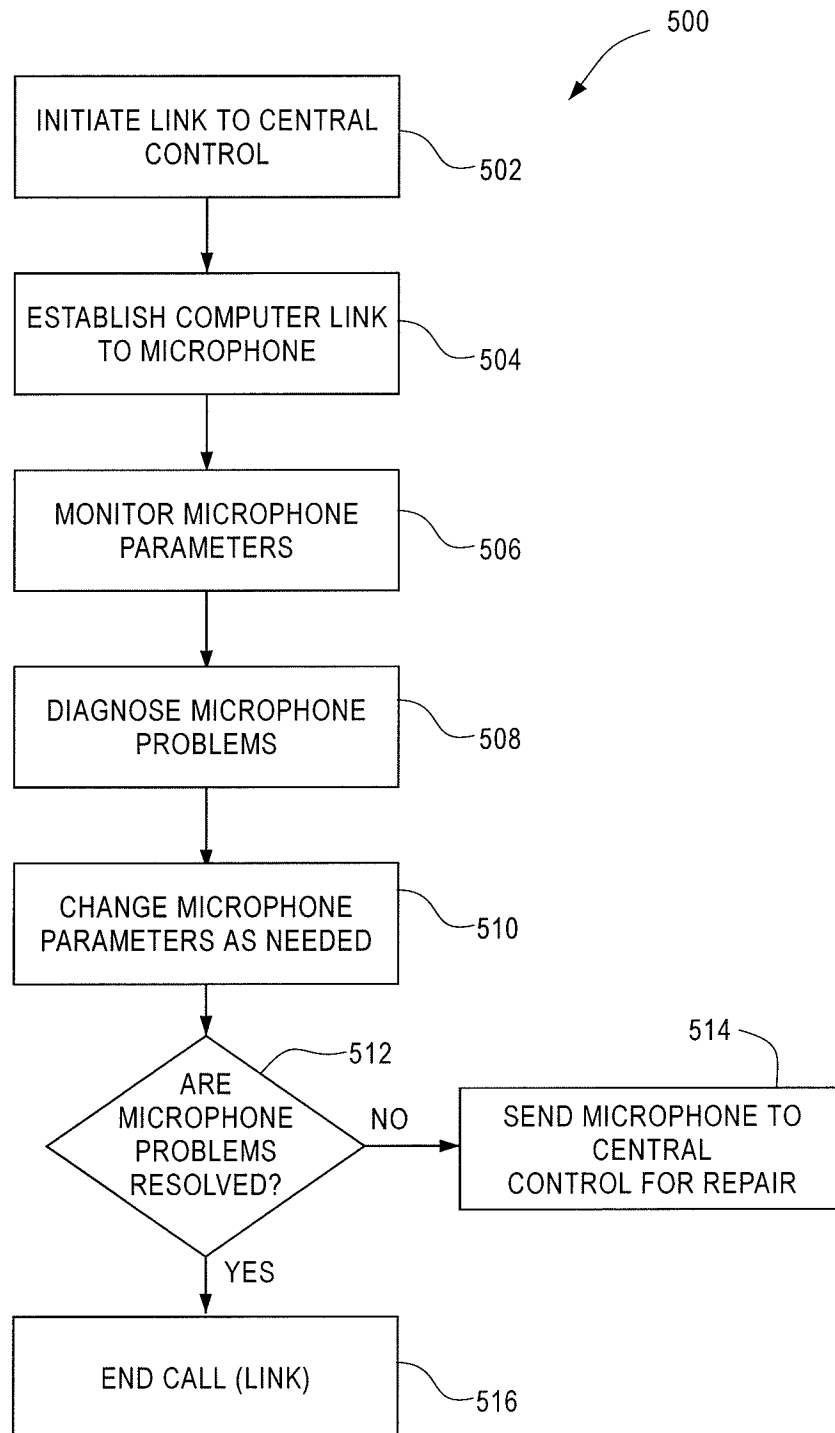
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Fig. 4



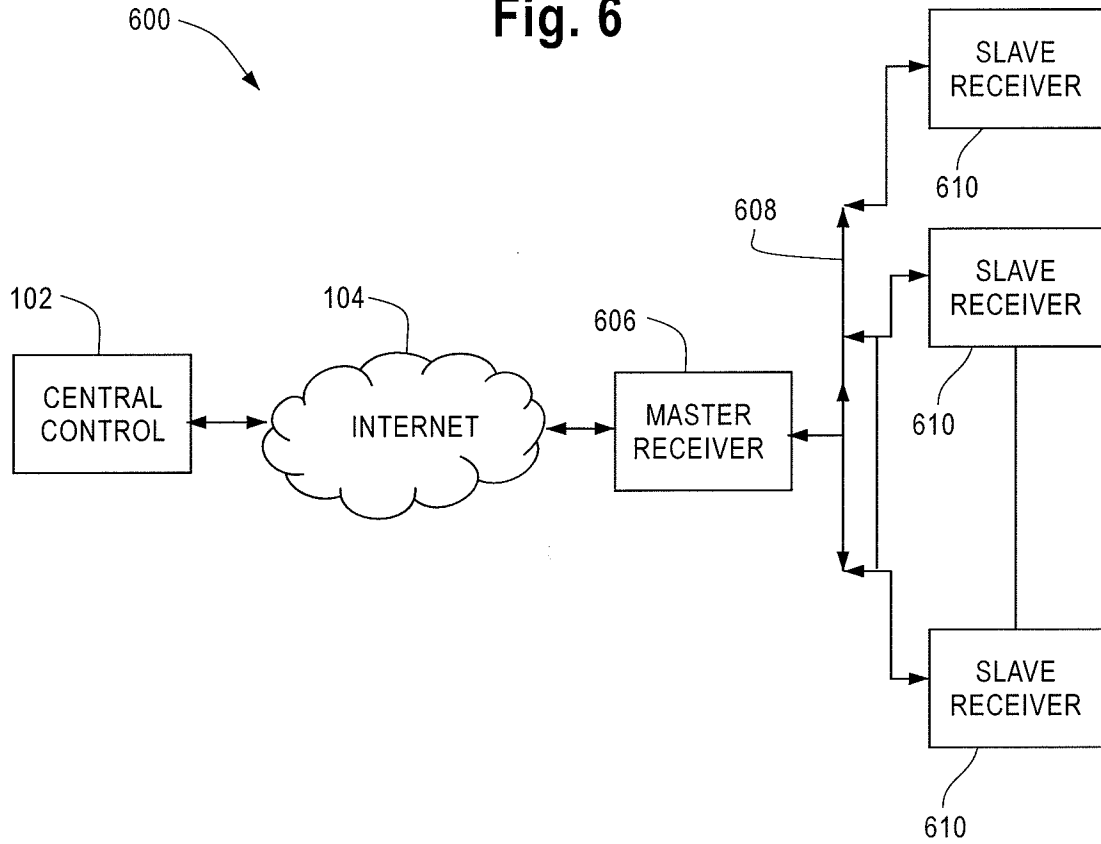
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**Fig. 5**

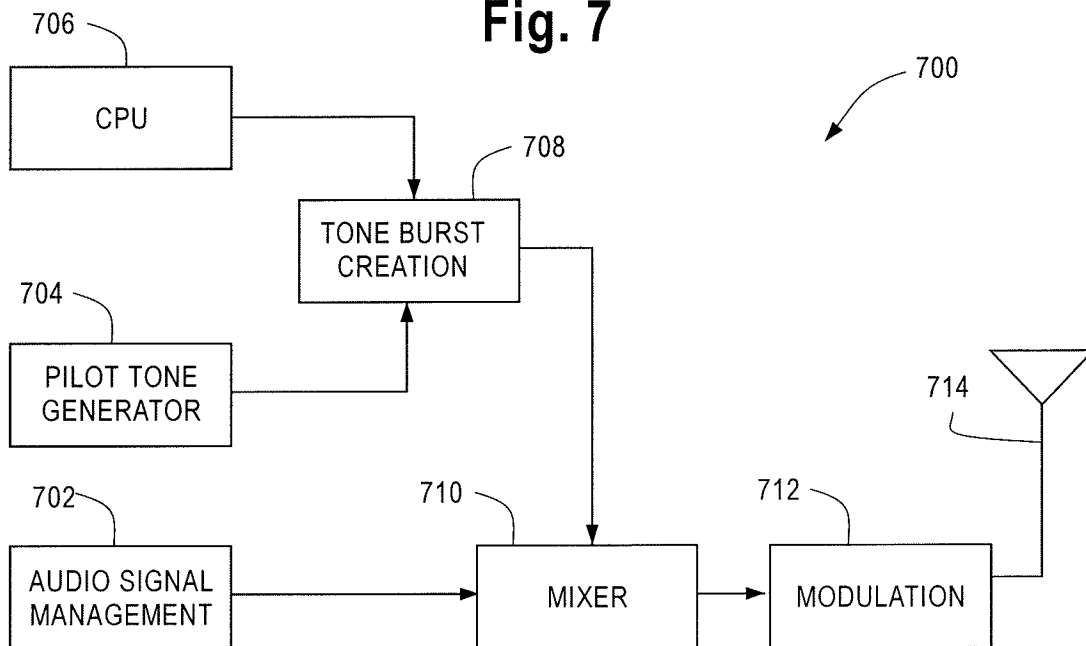


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**Fig. 6**

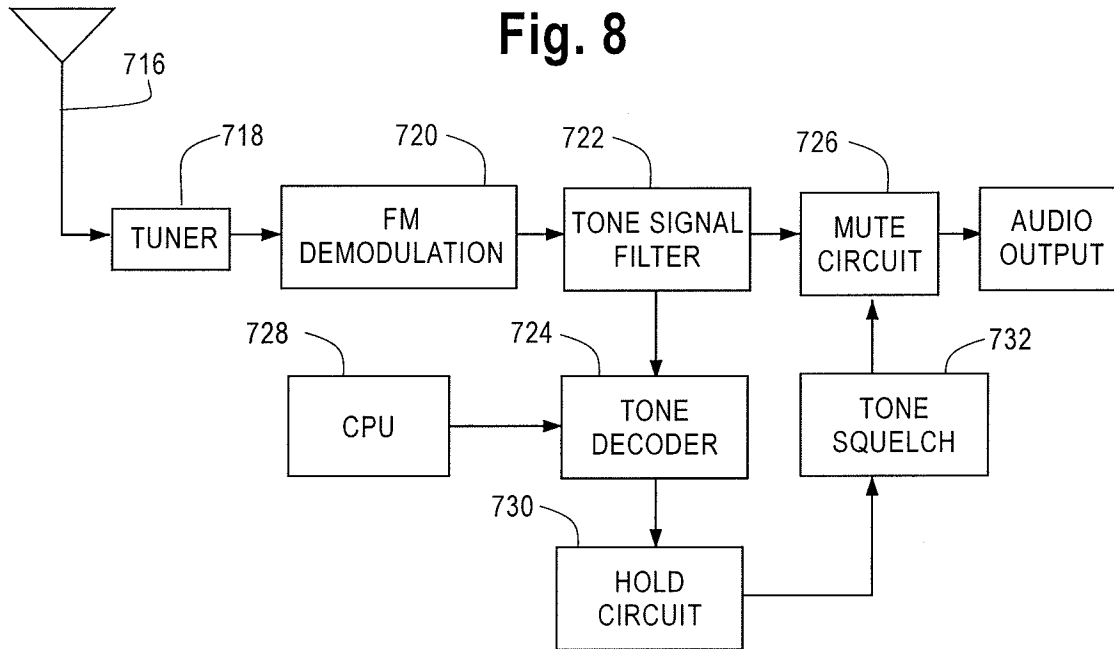


**Fig. 7**

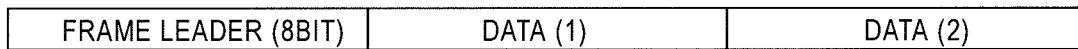


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**Fig. 8**



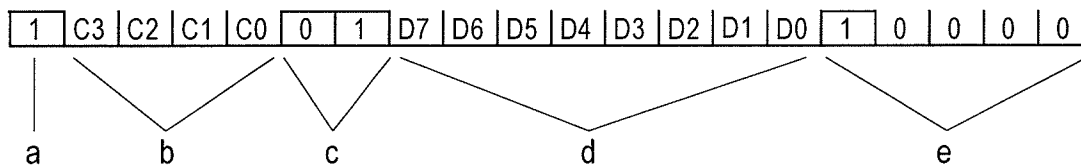
**Fig. 9A**



**Fig. 9B**



**Fig. 9C**



- a. START BIT (1BIT)
- b. INDEX (4BIT)
- c. SEPARATOR (2BIT)
- d. DATA (8BIT)
- e. STOP SEQUENCE (5BIT)